

# SPECIFICATIONS FOR LCD MODULE



## MODEL NO. BG12864EBNHHn\$ VER.03

| FOR MESSRS:  |  |  |
|--------------|--|--|
| ON DATE OF:  |  |  |
| APPROVED BY: |  |  |

**BOLYMIN, INC.** 

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## **History of Version**

| Version | Contents  | Date       | Note                  |
|---------|---|------------|-----------------------|
| 01      | NEW VERSION   | 2013/11/21 | SPEC.                 |
| 02      | Modify Handling Instruction · Electrical Characteristics  | 2014/05/30 | Page 6 · 8            |
| 03      | Modify Absolute Maximum Rating · Optical Characteristics · Quality Assurance · Reliability · Drawing. | 2018/12/19 | Page<br>5~10<br>14~17 |
|         | BOLYI   | VII        | N                     |
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|         |   |            |                       |



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1. Numbering System

|  | <u>B</u> | <u>G</u> | 12864 | <u>E</u> | <u>B</u> | <u>N</u> | <u>H</u> | = | <u>H</u> | <u>n\$</u> |
|--|----------|----------|-------|----------|----------|----------|----------|---|----------|------------|
|  | 0        | 1        | 2     | 3        | 4        | 5        | 6        | 7 | 8        | 9          |

| 0 | Brand   | Bolymin   |  |
|---|---|---|--|
| 1 | Module Type                                       | C= character type G= graphic type P= TAB/TCP type   | O= COG type<br>F= COF type<br>L=PLED/OLED  |
| 2 | Format  | 2002=20 characters, 2 lines<br>12232= 122 x 32 dots   |  |
| 3 | Version No.                                       | A type  |  |
| 4 | LCD Color   | G=STN/gray Y=STN/yellow-green PLED/yellow-green C=color STN,OLED/RGB  | B=STN/blue,OLED/blue<br>F=FSTN<br>T=TN   |
| 5 | LCD Type  | R=positive/reflective<br>P=positive/transflective   | M=positive/transmissive<br>N=negative/transmissive   |
| 6 | Backlight type/color                              | L=LED array/ yellow-green H=LED edge/white R=LED array/red G=LED edge/yellow-green F=RGB array I=RGB edge Q=LED edge/red N=No backlight | D=LED edge/blue E=EL/white B=EL/blue C=CCFL/white Y=LED Bottom/yellow O=LED array/orange K=LED edge/green A=LED edge/amber |
| 7 | CGRAM Font<br>(applied only on<br>character type) | J=English/Japanese Font<br>E=English/European Font<br>G=Chinese(simple)<br>F=Chinese(traditional)                                       | C=English/Cyrillic Font<br>H=English/Hebrew Font<br>A=English/Arabic Font  |
| 8 | View Angle/ Operating Temperature                 | B=Bottom/Normal Temperature H=Bottom/Wide Temperature U=Bottom/Ultra wide Temperature   | T=Top/Normal Temperature W=Top/Wide Temperature C=9H/Normal Temperature E=Top/ultra wide temperature                       |
| 9 | Special Code                                      | 3=3.3 volt logic power supply<br>n=negative voltage for LCD<br>c=cable/connector<br>207d=AVANT IC                                       | t=temperature compensation<br>for LCD<br>p=touch panel<br>\$=RoHS  |



### 2. Handling Precaution

#### 2.1 Precaution in use of LCD Module

- 2.1.1. LCD panel is made of glass. Avoid excessive mechanical shock or applying strong pressure and/or sharp tools on the surface of display area.
- 2.1.2. The polarizer placed on the display surface is easily scratched and damaged. Extreme care should be taken when handling it. To clean dust or dirt off the display surface, wipe gently with cotton, or other soft material soaked with isopropyl alcohol, ethyl alcohol, do not use water, ketone or aromatics to clear display surface, and never scrub it hard.
- 2.1.3. Keep LCD panels away from direct sunlight. The storage environment should be dust-free, clean, dry, temperature is 25°C ±10°C and the humidity is below 55% RH.
- 2.1.4. Do not input any signal before power is turned on.
- 2.1.5. Avoid pressing on the metal bezel, otherwise the elastomer connector could be deformed and lose contact, resulting in missing pixels and also cause rainbow on the display.
- 2.1.6. It's important to control soldering temperature and time. RoHS compliant materials might need higher temperature and time, but try to keep temperature under 350°C and time in 3-5 sec.
- 2.1.7. EL is manufactured from the organic film, and is easily affected by temperature, humidity and other environmental impact. Long time storage might cause low quality of the case. Therefore, please start production in 3 months after reception of the LCM. If in any case, long time storage over 3 months is necessary, please keep EL in vacuum package or at least in humidity < 35% RH, and temperature 25°C±10°C. Note: 2.1.7. is applied to EL backlight only.

## 2.2 Static Electricity Precautions:

- 2.2.1. The LCD module contains a C-MOS LSI. People who operate the LCM should wear ESD protection equipment to prevent ESD hurt on products.
- 2.2.2. Do not touch any of the conductive parts such as the LSI pads; the copper leads on the PCB and the interface terminals with any parts of the human body.
- 2.2.3. Do not touch the connection terminals of the display with bare hand; it will cause disconnection or defective insulation of terminals.
- 2.2.4. The modules should be kept in anti-static bags or trays for storage.
- 2.2.5. Only properly grounded soldering irons should be used.
- 2.2.6. If an electric screwdriver is used, it should be grounded and shielded to prevent sparks.
- 2.2.7. The normal static prevention measures should be observed for work clothes and working benches.
- 2.2.8. Since dry air(almost low RH) is inductive to static, a humidity of 50-60% RH is recommended in assembly line.

## 2.3 Operation Precautions:

- 2.3.1. DC voltage applied on LCM causes electrochemical reactions, which will deteriorate the display over time. The applied pulse waveform should be a symmetric waveform such that no DC component remains. Be sure to use the specified operating voltage.
- 2.3.2. LCD driving voltage should be kept within specified range; excess voltage will shorten display life, while less voltage may not turn on LCM.
- 2.3.3. LCM response time will be extremely delayed in low operating temperature(such as -20 °C) than in room operating temperature. Therefore, higher LCD driving voltage is required in low operating temperature; On the other hand, in high operating temperature (such as +70°C) LCD shows dark background color, therefore lower LCD driving voltage is required. Be sure to use the specified LCD driving voltage in different operating temperature.



### 2.4 Safety:

2.4.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin. If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

#### 2.5 WARRANTY POLICY

Bolymin .Will provide one-year warranty for the products only if under specification operating conditions.

If there are functional defects found during the period of warranty, the defective products would be replaced on a one-to-one basis.

Bolymin would not be responsible for any direct/indirect liabilities consequential to any parties.

#### **2.6 MTBF**

- 2.6.1 .By specific test condition, MTBF based on 30°C normal operation temperature is 50,000hours. ■
- 2.6.2 Test Condition:
  - 2.6.2.1 Supply Voltage for LCM: Typical Vdd
  - 2.6.2.2 CC (Constant Current) mode and typical current is applied for LED.
  - 2.6.2.3 Run-Patterns: by Bolymin's test program that has defined patterns and cyclic period.
  - 2.6.2.4 Humidity: 60%RH
- 2.6.3 Test Criteria:

Attenuation of average brightness: ≤ 50%

Increasing of current consumption for LCM/Backlight: ≤ 20%

Display function at room temperature: Normal

Appearance: Normal



## 3. General Specification

## (1) Mechanical Dimension

| Item                     | Standard Value                | Unit |
|--------------------------|-------------------------------|------|
| Number of dots           | 128×64                        | dots |
| Module dimension (L*W*H) | 75.0 x 52.7 x 8.9 (edge LED ) | mm   |
| View area                | 60(W)×32.6(H)                 | mm   |
| Active area              | 55.0(W)×27.48(H)              | mm   |
| Dot size                 | 0.39(W)×0.39(H)               | mm   |
| Dot pitch                | 0.43(W)×0.43(H)               | mm   |

## (2) Controller IC: NT7107/NT7108

## 4.1 Electrical Absolute Maximum Ratings

(Vss=0V, Ta=25°C)

| Item                     | Symbol                               | Min | Тур | Max         | Unit |
|--------------------------|--------------------------------------|-----|-----|-------------|------|
| Input Voltage            | $V_{\rm I}$                          | 0   | _   | $V_{ m DD}$ | V    |
| Supply Voltage For Logic | $V_{DD}$                             | 0   | _   | 6.7         | V    |
| Supply Voltage For LCD   | $V_{\mathrm{DD}}$ - $V_{\mathrm{O}}$ | 0   | _   | 16.7        | V    |
| Supply Voltage For LCD   | $V_{\rm EE}$                         | _   | _   | -10         | V    |



## 4.2 Environmental Absolute Maximum Ratings

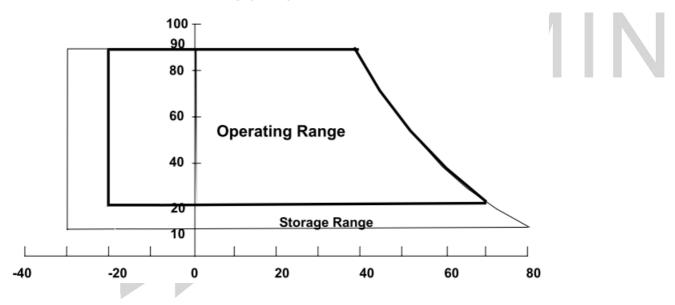
| Item                  | Symbol | Min | Max | Unit                    | Note          |
|-----------------------|--------|-----|-----|-------------------------|---------------|
| Operating Temperature | TOP    | -20 | 70  | $^{\circ}\! \mathbb{C}$ | (1)           |
| Storage Temperature   | TST    | -30 | 80  | $^{\circ}\! \mathbb{C}$ | (1) \cdot (2) |

### Note (1)

- (a) 90 %RH Max. (Ta <= 40 °C).
- (b) Wet-bulb temperature should be 39 °C Max. (Ta > 40 °C).
- (c) No condensation.

Note (2) The temperature of panel surface should be -20 °C min. and 70 °C max.

## Relative Humidity (%RH)





## 5. Electrical Characteristics

| Item                          | Symbol                                | Condition                                | Min.                 | Тур. | Max.        | Unit              |
|-------------------------------|---------------------------------------|--|----------------------|------|-------------|-------------------|
| Supply Voltage For Logic      | $V_{\mathrm{DD}}$ - $V_{\mathrm{SS}}$ | _  | 4.75                 | 5.0  | 5.25        | V                 |
| Supply Voltage For LCD        | $V_{\mathrm{DD}}$ - $V_{\mathrm{0}}$  | Ta=25°C                                  | 8.2                  | 8.5  | 8.8         | V                 |
| Input High Vol                | $V_{\mathrm{IH}}$                     | _  | V <sub>DD-</sub> 2.2 | _    | $V_{ m DD}$ | V                 |
| Input Low Vol                 | $V_{\mathrm{IL}}$                     | _  | 0                    | _    | 0.8         | V                 |
| Output High Vol               | $V_{OH}$                              | _  | V <sub>DD</sub> -0.3 | _    | $V_{ m DD}$ | V                 |
| Output Low Vol.               | $V_{\mathrm{OL}}$                     | _  | 0                    | _    | 0.3         | V                 |
| Supply Current                | $I_{DD}$                              | _  | _                    | 5.0  | _           | mA                |
| LCM Surface Luminance Ta=25°C | L                                     | I <sub>LED</sub> =80mA<br>Display all ON | 110                  | 165  | _           | cd/m <sup>2</sup> |

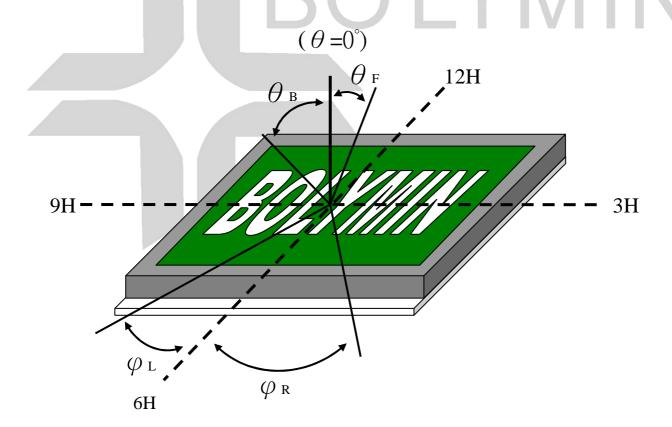
<sup>※</sup>Optimum LCD driving voltage value, referring to above mentioned range, is changed due to
different batch of LCD glass.



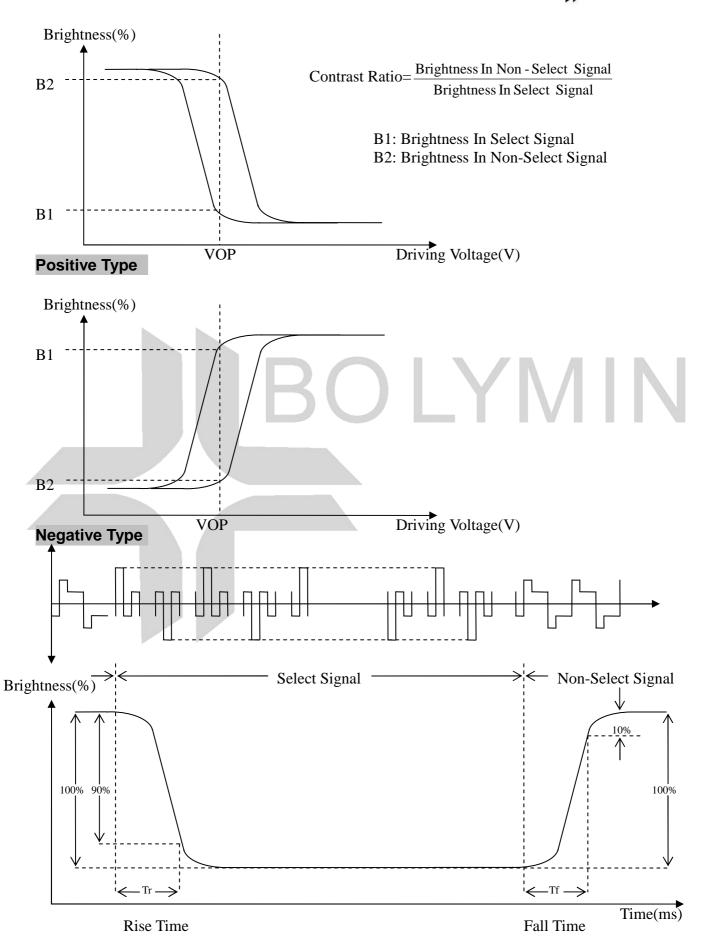
## 6. Optical Characteristics

a. STN  $(Ta=25^{\circ}C)$ 

| Item               | Symbol                | Min. | Тур. | Max. | Unit |
|--------------------|-----------------------|------|------|------|------|
|                    | heta f                | -    | 30   | -    | deg  |
|                    | heta B                | -    | 35   | -    | deg  |
| View Angle (CR>=2) | <i>φ</i> <sub>L</sub> | -    | 38   | -    | deg  |
|                    | arphi R               | -    | 40   | -    | deg  |
| Contrast Ratio     | CR                    | 2    | 3    | -    | -    |
| D T' 25°C          | T rise                | -    | 200  | 350  | ms   |
| Response Time 25°C | T fall                | -    | 200  | 400  | ms   |









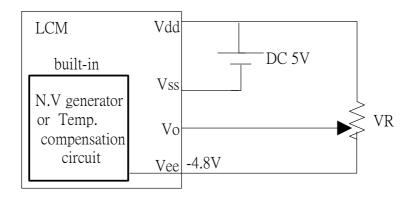
## 7. Interface Pin Function

|         | 11400 1 | - III Fullo |  |
|---------|---------|-------------|--|
| Pin No. | Symbol  | Level       | Description                              |
| 1       | Vdd     | 5.0V        | Supply voltage for logic                 |
| 2       | GND     | 0V          | Ground                                   |
| 3       | $V_{O}$ | (Variable)  | Operating voltage for LCD                |
| 4       | DB0     | H/L         | Data bus line                            |
| 5       | DB1     | H/L         | Data bus line                            |
| 6       | DB2     | H/L         | Data bus line                            |
| 7       | DB3     | H/L         | Data bus line                            |
| 8       | DB4     | H/L         | Data bus line                            |
| 9       | DB5     | H/L         | Data bus line                            |
| 10      | DB6     | H/L         | Data bus line                            |
| 11      | DB7     | H/L         | Data bus line                            |
| 12      | /CS1    | L           | Chip Select for IC1                      |
| 13      | /CS2    | L           | Chip Select for IC2                      |
| 14      | /RST    | L           | Reset signal                             |
| 15      | R/W     | H/L         | H=Read mode , L=Write mode               |
| 16      | D/I     | H/L         | H=Data register , L=Instruction register |
| 17      | E       | H/L         | Enable signal                            |
| 18      | Vee     |             | Negative Voltage output –4.8V            |
| 19      | A       | _           | Power supply for B/L ( + )               |
| 20      | K       | _           | Power supply for B/L (GND)               |



## 8. Power supply for LCD Module and LCD operating voltage adjustment

\*(Option) LCM operating on " DC 5V " input with built-in negative voltage



## 9. Backlight information

## 9.1 Specification

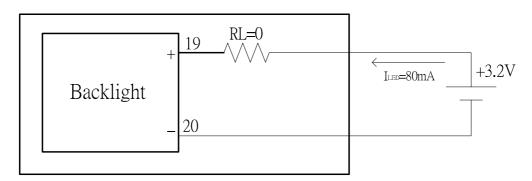
(1)LED edge/white

| (1)EEE cage, winte  |        |       |     |      |      |                |  |
|---------------------|--------|-------|-----|------|------|----------------|--|
| Parameter           | Symbol | Min   | Тур | Max  | Unit | Test Condition |  |
| Supply Current      | ILED   |       | 80  | _    | mA   | V=3.2V         |  |
| Supply Voltage      | V      | 2.8   | 3.2 | 3.6  | V    | ILED=80mA      |  |
| Reverse Voltage     | VR     | _     |     | 5    | V    | _              |  |
| CIE<br>Chromaticity | X      | 0.27  | _   | 0.32 |      |                |  |
| Diagram             | Y      | 0.27  | _   | 0.32 | nm   | ILED=80mA      |  |
| Color               |        | White |     |      |      |                |  |

### 9.2 Backlight driving methods

- a. B/L drive from pin19 (LED+) pin20 (LED-)
  - al LED edge/white

**LCM** 





## 10. Quality Assurance

## 10.1 Inspection conditions

- 1. The LCD shall be inspected under 20~40W white fluorescent light.
- 2. Checking Direction shall be in the 40 degree from perpendicular line of specimen surface.
- 3. Checker shall see over 30 cm.
- 4. Inspect about 5 seconds for each side.
- 5. Defect that is located at outside of VA and doesn't affect function is ignored.

## **10.2 Inspection Parameters**

|     | ·                                     |  |   |             |  |  |            |  |  |
|-----|---------------------------------------|--|---|-------------|--|--|------------|--|--|
| NO. | Parameter                             |  | Criteria                                  |             |  |  |            |  |  |
| 1   | Black or<br>White spots<br>(Particle) | Dimension $D \le 0.10$ $0.10 < D \le$ $0.2 < D \le 0$ $0.3 < D$ $D = (Long + S)$ $Total defects$ | 0.2<br>0.3<br>0.3<br>Short)/2<br>s should | I not excee |  | Acceptable<br>Level  2.5  doesn't affect for | unction is |  |  |
| 2   | Scratch,<br>Substances                | X(mm) Y  - 0.  4.0≥L 0.  3.0≥L 0  - 0  X: Length  Total defects                                  |   |             |  |  |            |  |  |



| 3 | Air Bubbles<br>( between<br>glass &<br>polarizer) | $ \begin{array}{ c c c c c c } \hline Zone & Acceptable & Class Of & Acceptable \\ \hline Dimension & Number & Defects & Level \\ \hline \hline D \leqq 0.2 & Disregard \\ \hline 0.2 < D \leqq 0.5 & 3 & Minor & 2.5 \\ \hline \hline 0.5 < D & 0 & \\ \hline \hline Total defects shall not excess 3/module. \\ \hline Defect that is located at outside of AA and doesn't affect function is ignored. \\ \hline Bobble is sawn only under reflection light is disregarded. \\ \hline                                  $ |
|---|---|--|
| 4 | Displaying  | 1. Incomplete or broken line is not allowed. Pinholes  |
|   | Pattern   | Dimension Φ(mm)  |

Other Inspection standard reference Bolymin standard.



## 11. Reliability

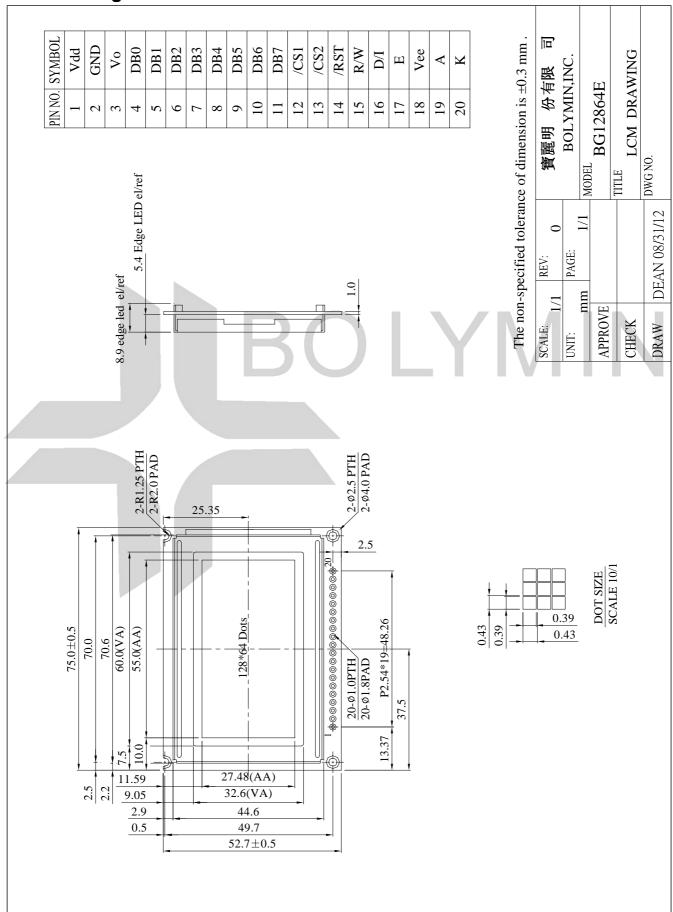
## **■**Content of Reliability Test

| Envi | ronmental Test                    |  |  |                        |
|------|-----------------------------------|--|--|------------------------|
| No   | Test Item                         | Content of Test  | Test Condition   | Applicable<br>Standard |
| 1    | High Temperature storage          | Endurance test applying the high storage temperature for a long time.  | 80°C<br>96 hrs   |                        |
| 2    | Low Temperature storage           | Endurance test applying the high storage temperature for a long time.  | -30°C<br>96 hrs  |                        |
| 3    | High Temperature Operation        | Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time. | 70℃<br>96 hrs  |                        |
| 4    | Low Temperature Operation         | Endurance test applying the electric stress under low temperature for a long time.                                     | -20°C<br>96 hrs  |                        |
| 5    | Humidity Test                     | Endurance test applying the high humidity storage for a long time.   | 40°C,90%RH<br>96hrs  |                        |
| 6    | Temperature cycle (Non-operation) | Endurance test applying the low and high temperature cycle30°C 80°C  4 30min 30min 1 cycle                             | -30°C/80°C<br>10 cycles  |                        |
| 7    | Vibration test                    | Endurance test applying the vibration during transportation and using.   | Total Fixed Amplitude:1.5mm  Vibration Frequency:10~55Hz One cycle 60 seconds to 3 direction of X,Y,Z for each 15minutes |                        |

 $<sup>\</sup>divideontimes$ Assess after placing at normal temperature and humidity for 4 hour  $\circ$  No abnormalities in functions and appearance  $\circ$ 



## 12. Appendix (Drawing , IC controller data) 12.1 Drawing





## 12.2 IC controller data

## 12.2.1 Display Control Instruction

The display control instructions control the internal state of the NT7108. Instruction is received from MPU to NT7108 for the display control. The following table shows various instructions.

| Instruction             | D/I | R/W | DB7         | DB6   | DB5        | DB4              | DB3     | DB2   | DB1  | DB0 | Function   |
|-------------------------|-----|-----|-------------|-------|------------|------------------|---------|-------|------|-----|--|
| Display ON/OFF          | 0   | 0   | 0           | 0     | 1          | 1                | 1       | 1     | 1    | 0/1 | Controls the display on or off. Internal status and display RAM data are not affected.                                   |
|                         |     |     |             |       |            |                  |         |       |      |     | 0:OFF, 1:ON  |
| Set Address             | 0   | 0   | 0           | 1     | Y ado      | dress            | (0~6.   | 3)    |      |     | Sets the Y address in the Y address counter.   |
| Set Page<br>(X address) | 0   | 0   | 1           | 0     | 1          | 1                | 1       | Page  | (0~7 | 7)  | Sets the X address at the X address register.  |
| Display Start Line      | 0   | 0   | 1           | 1     | Disp       | lay st           | art lir | ne(0~ | 63)  |     | Indicates the display data RAM displayed at the top of the screen.   |
|                         |     |     | В           |       |            | R                |         |       |      |     | Read status.  BUSY 0:Ready   |
| Status Read             | 0   | 1   | U<br>S<br>Y | 0     | ON/<br>OFF | E<br>S<br>E<br>T | 0       | 0     | 0    | 0   | 1:In operation ON/OFF 0:Display ON 1:Display OFF RESET 0:Normal  |
| Write Display<br>Data   | 1   | 0   | Disp        | lay D | ata        |                  |         |       |      |     | 1:Reset  Writes data (DB0:7)into display data RAM. After writing instruction, Y address is increased by 1 automatically. |
| Read Display<br>Data    | 1   | 1   | Disp        | lay D | ata        |                  |         |       |      |     | Reads data (DB0:7) from display data RAM to the data bus.  |



#### **Detailed Explanation**

Display On/Off

| R/ | W | D/I | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|----|---|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| (  | ) | 0   | 0   | 0   | 1   | 1   | 1   | 1   | 1   | D   |

The display data appears when D is and disappears when D is 0. Though the data is not on the screen with D = 0, it remains in the display data RAM. Therefore, you can make it appear by changing D = 0 into D = 1.

Display Start Line

| R/W | D/I | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 0   | 0   | 1   | 1   | A   | A   | A   | A   | A   | A   |

Z address AAAAA ( binary ) of the display data RAM is set in the display start line register and displayed at the top of the screen. Figure 2. shows examples of display ( 1/64 duty cycle ) when the start line = 0-3. When the display duty cycle is 1/64 or more ( ex. 1/32, 1/24 etc. ), the data of total line number of LCD screen, from the line specified by display start line instruction, is displayed.

Set Page (X Address)

| R/W | D/I | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 0   | 0   | 1   | 0   | 1   | 1   | 1   | A   | Ā   | A   |

X address AAA (binary) of the display data RAM is set in the X address register. After that, writing or reading to or from MPU is executed in this specified page until the next page is set. See Figure 1.

## Set Y Address

| R/W | D/I | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 0   | 0   | 0   | 1   | A   | A   | A   | A   | A   | A   |

Y address AAAAAA (binary) of the display data RAM is set in the Y address counter. After that, Y address counter is increased by 1 every time the data is written or read to or from MPU.

#### Status Read

| R/W | D/I | DB7  | DB6 | DB5    | DB4   | DB3 | DB2 | DB1 | DB0 |
|-----|-----|------|-----|--------|-------|-----|-----|-----|-----|
| 0   | 1   | Busy | 0   | On/Off | RESET | 0   | 0   | 0   | 0   |



#### Busy

When busy is 1, the LSI is executing internal operations. No instruction are accepted while busy is 1, so you should make sure that busy is 0 before writing the next instruction.

#### •ON/OFF

Shows the liquid crystal display condition: on condition or off condition.

When on/off is 1, the display is in off condition.

When on/off is 0, the display is in on condition.

#### RESET

RESET = 1 shows that the system is being initialized. In this condition, no instructions except status read can be accepted.

RESET = 0 shows that initializing has system is in the usual operation condition.

Write Display Data

| R/W | D/I | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 0   | 1   | D   | D   | D   | D   | D   | D   | D   | D   |

Writes 8-bit data DDDDDDDD (binary) into the display data RAM. The Y address is increased by 1 automatically.

Read Display Data

| R/W | D/I | DB7 | DB6 | DB5 | DB4 | DB3 | DB2 | DB1 | DB0 |
|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 1   | 1   | D   | D   | D   | D   | D   | D   | D   | D   |

Reads out 8-bit data DDDDDDD (binary) from the display data RAM. Then Y address is increased by 1 automatically.



One dummy read is necessary right after the address setting. For details, refer to the explanation of output register in "Function of Each Block".

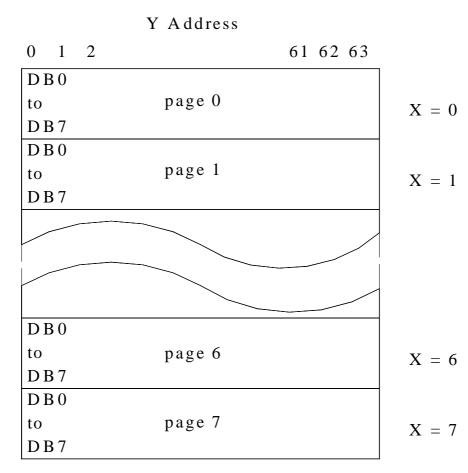
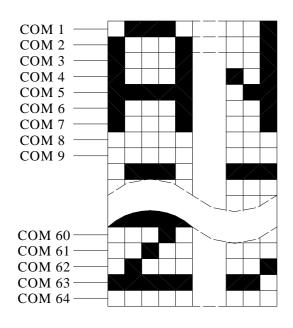
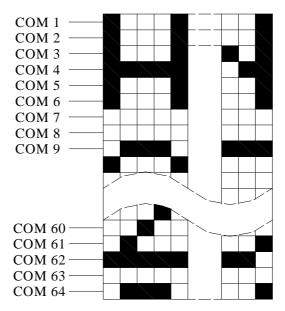


Figure 1

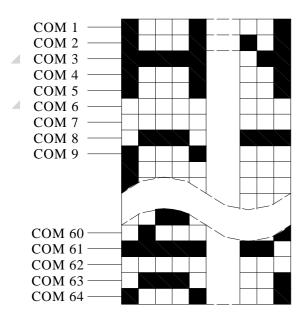


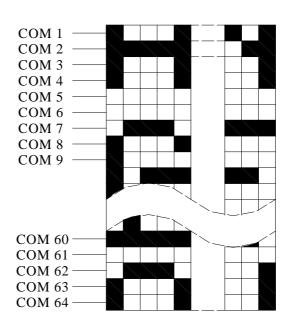




Start line = 0

Start line = 1





Start line = 3

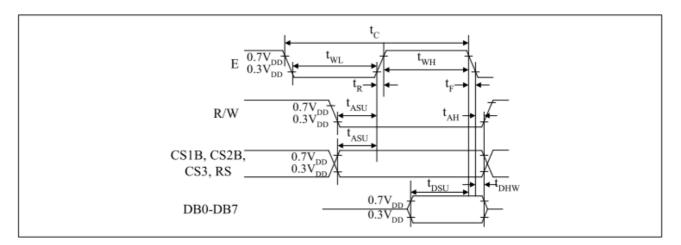
Start line = 4

Figure 2

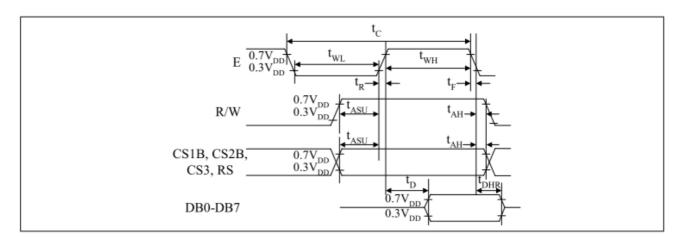


12.2.2 Timing characteristics

| Characteristic         | Symbol | Min  | Type | Max | Unit |
|------------------------|--------|------|------|-----|------|
| E cycle                | tc     | 1000 | -    | -   |      |
| E high level width     | twн    | 450  | -    | -   |      |
| E low level width      | twl    | 450  | -    | -   | ]    |
| E rise time            | tr     | -    | -    | 25  | ]    |
| E fall time            | tF     | -    | -    | 25  |      |
| Address set-up time    | tasu   | 140  | -    | -   | ns   |
| Address hold time      | tah    | 10   | -    | -   | ]    |
| Data set-up time       | tdsu   | 200  | -    | -   | ]    |
| Data delay time        | to     | -    | -    | 320 |      |
| Data hold time (write) | tohw   | 10   | -    | -   | ]    |
| Data hold time (read)  | tohr   | 20   | -    | -   |      |



**MPU Write Timing** 



**MPU Read Timing**